

SCHEMATICS

5250

tau-tron

A UNIT OF
GENERAL SIGNAL





SCHEMATICS

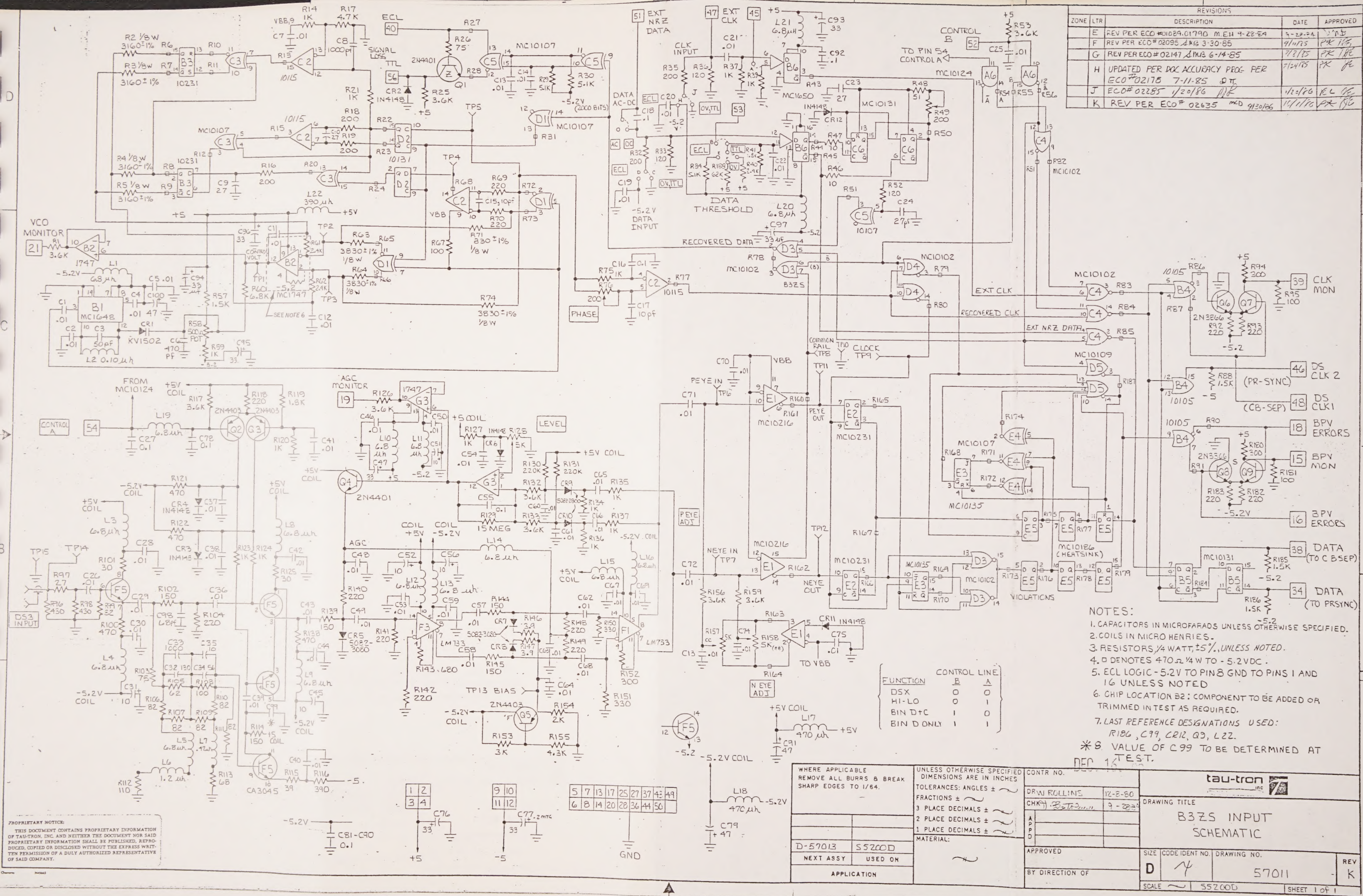
5250



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REVISIONS				DATE	APPROVED
ZONE	LTR	DESCRIPTION			
E	REV PER ECO #01089.01790	M.E.H 4-28-84	3-28-84	PK 106	
F	REV PER ECO #02095	AMG 3-30-85	4/1/85	PK 106	
G	REV PER ECO #02147	AMG 6-14-85	7/8/85	PK 106	
H	UPDATED PER DOC ACCURACY PROJ PER	ECO #02178 7-11-85	8/1/85	PK 106	
J	ECO #02285	1/20/86	1/20/86	PK 106	
K	REV PER ECO #02635	MD 9/30/86	10/1/86	PK 106	



NOTES:

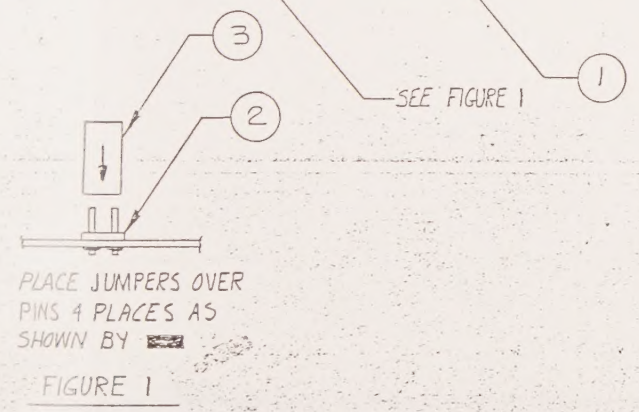
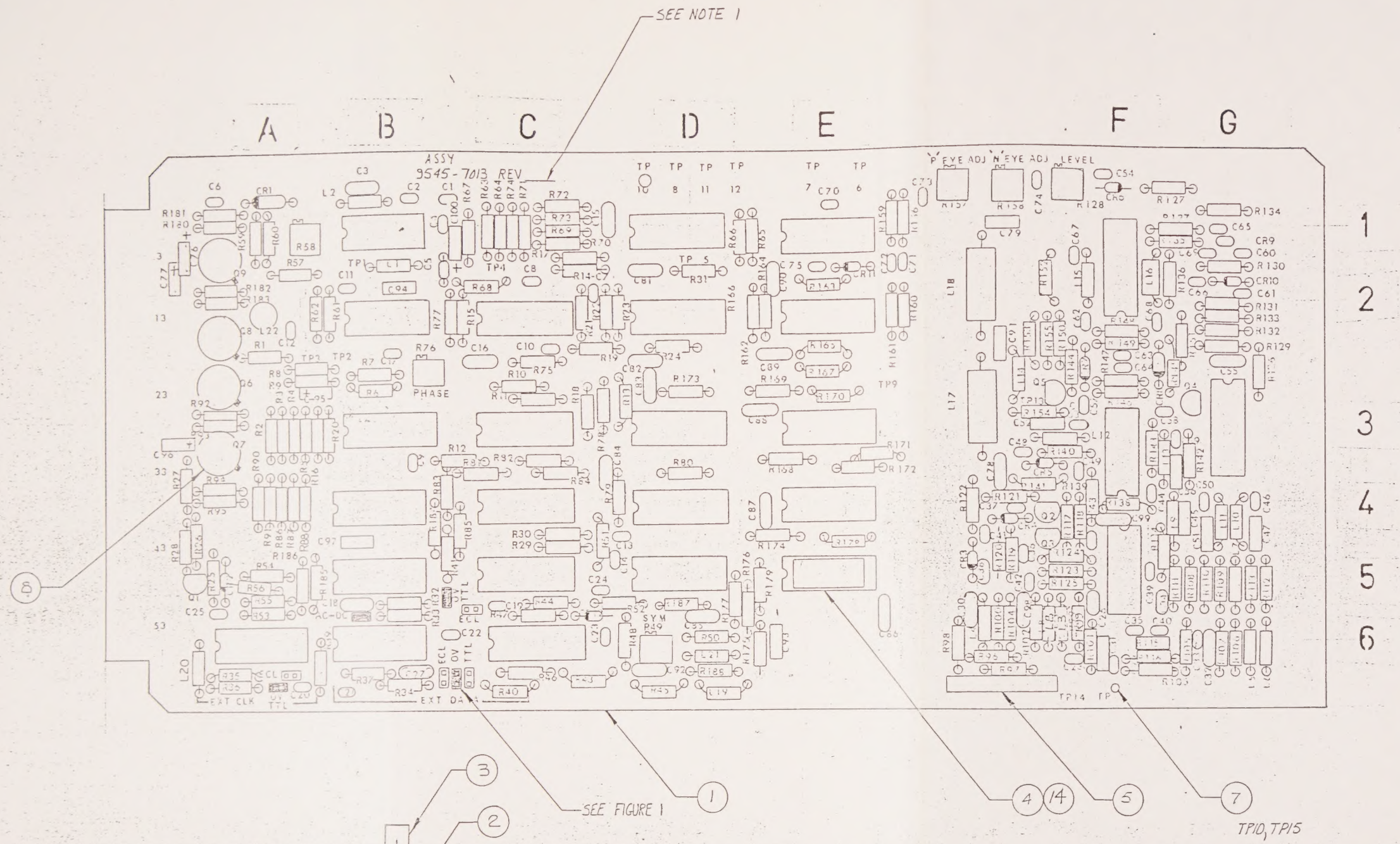
1. CAPACITORS IN MICROFARADS UNLESS OTHERWISE SPECIFIED.
2. COILS IN MICRO HENRIES.
3. RESISTORS $\frac{1}{4}$ WATT $\pm 5\%$, UNLESS NOTED.
4. $\square$ DENOTES $470\Omega \frac{1}{4}W$ TO $-5.2VDC$.
5. ECL LOGIC - $-5.2V$ TO PIN 8 GND TO PINS 1 AND 16 UNLESS NOTED.
6. CHIP LOCATION B2: COMPONENT TO BE ADDED OR TRIMMED IN TEST AS REQUIRED.
7. LAST REFERENCE DESIGNATIONS USED:
R186, C79, CR12, Q3, L22.
- * 8. VALUE OF C99 TO BE DETERMINED AT TEST.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CONTR NO.		tau-tron	
TOLERANCES: ANGLES $\pm$		FRACTIONS $\pm$		DRAW ROLLING		12-8-80	
3 PLACE DECIMALS $\pm$		2 PLACE DECIMALS $\pm$		CHKD		9-28-83	
1 PLACE DECIMALS $\pm$		MATERIAL:		APPROVED		BY DIRECTION OF	
D-57013		S5200D		SIZE		CODE IDENT NO.	
NEXT ASSY		USED ON		D		57011	
APPLICATION				SCALE		S5200D	
						SHEET 1 of 1	

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K	UPDATED & REDRAWN PER DOC ACCURACY PROG PER ECO# 02178 7-3-85 DH	7/3/85	PR	✓
L	REV PER ECO# 02208 TMM 8-15-85	8/15/85	RL	✓
M	REV PER ECO# 02262 TMM 10/9/85	11/8/85	EX	✓
N	REV PER ECO# 02263 TMM 11/24/85	1/24/86	EX	✓

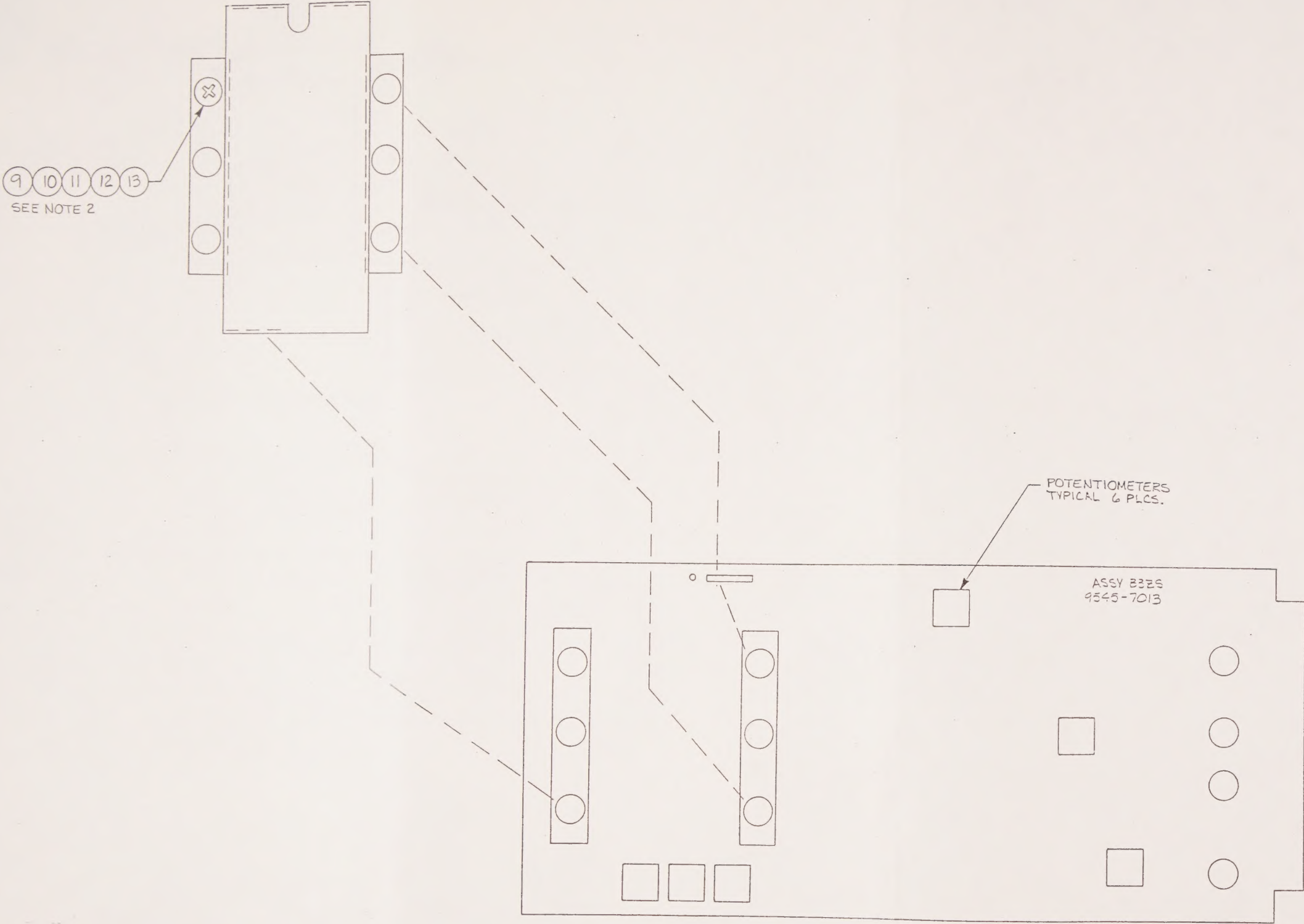


- NOTES:
1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
 2. INSTALL SCREWS FROM TOP SIDE.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES = γ FRACTIONS = γ 3 PLACE DECIMALS = γ 2 PLACE DECIMALS = γ 1 PLACE DECIMALS = γ MATERIAL:		CONTR NO DEC. 16. 86		tau-tron ALLOY CLYDEAL SIGNAL	
D-57626 55250		DR MOWRY 11-20-80		CHY B. COOPER 12-8-80		DRAWING TITLE	
E-57158 55200E RX		W. ROLLINS 12-8-80				B3 ZS INPUT PCB ASSEMBLY	
D-57350 55200D RX							
NEXT ASSY USED ON							
APPLICATION							
		APPROVED		DATE CODE IDENT NO		DRAWING NO	
		BY		D 984		57013	
				2/1		5200D RX	
						REV N	

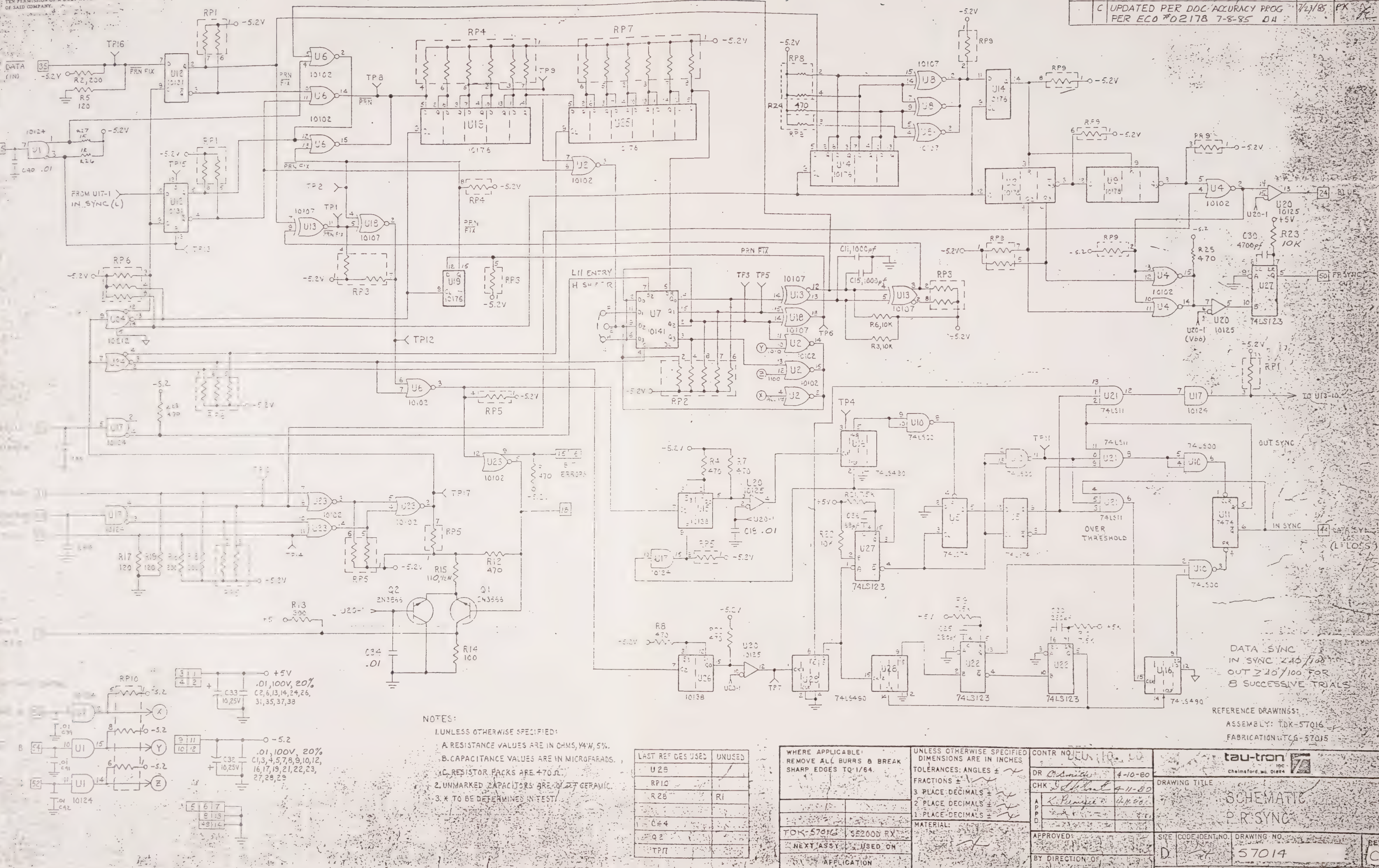
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INC.

REVISIONS			
ZONE	LTR	DESCRIPTION	DATE
SEE SHEET ONE FOR REVISIONS			



WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES $\pm$ $\frac{1}{2}^\circ$ FRACTIONS $\pm$ $\frac{1}{16}$ 3 PLACE DECIMALS $\pm$ $\frac{1}{1000}$ 2 PLACE DECIMALS $\pm$ $\frac{1}{500}$ 1 PLACE DECIMALS $\pm$ $\frac{1}{100}$ MATERIAL:		CONTR NO. 16. '86		tau-tron A UNIT OF GENERAL SIGNAL	
D-57626 S5250		E-57158 S5200S RX		D-57350 S5200D ZX		DRAWING TITLE B3ZS INPUT PCB ASSEMBLY	
NEXT ASSY USED ON		APPLICATION		APPROVED BY DIRECTION OF		SIZE CODE IDENT NO. DRAWING NO. D 57013	
						SCALE 2/1 5200D RX SHEET 2 OF 2	

ZONE	LTR	REVISIONS	DATE	APPROVED
	3	REVISED AND REDRAWN C.S. 4-10-80		
	4	REV PER PDC. 00795 M.D.C.	10-1-80	
A	B	REV & RELEASE PER DCN 00793 M.D.C. 7/7/81	1-22-81	WAS
	B	REV. PER ECO #1338 10-24-81 25FEB83	2-5-83	RLD
C	C	UPDATED PER DOC. ACCURACY PROG PER ECO #02178 7-8-85 D4	7/3/85	PX



LAST REF DES USED	UNUSED
U25	
RP10	
R25	RI
C44	
Q2	
TPN	

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	
TOK-570MG	55200D RX
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES $\pm$
FRACTIONS $\pm$
3 PLACE DECIMALS $\pm$
2 PLACE DECIMALS $\pm$
1 PLACE DECIMALS $\pm$
MATERIAL:

CONTR NO.	4-10-80	
DR	C. Smith	4-10-80
CHK	S. P. Hall	4-11-80
APP.	K. B. Smith	4-11-80
	S. P. Hall	4-11-80
APPROVED:	[Signature]	
BY DIRECTION OF	[Signature]	

tau-tron		10C	
Chelmsford, Md. 01824			
DRAWING TITLE			
SCHEMATIC			
P-R SYNC			
SIZE	CODE IDENT. NO.	DRAWING NO.	REV.
D	7	57014	C

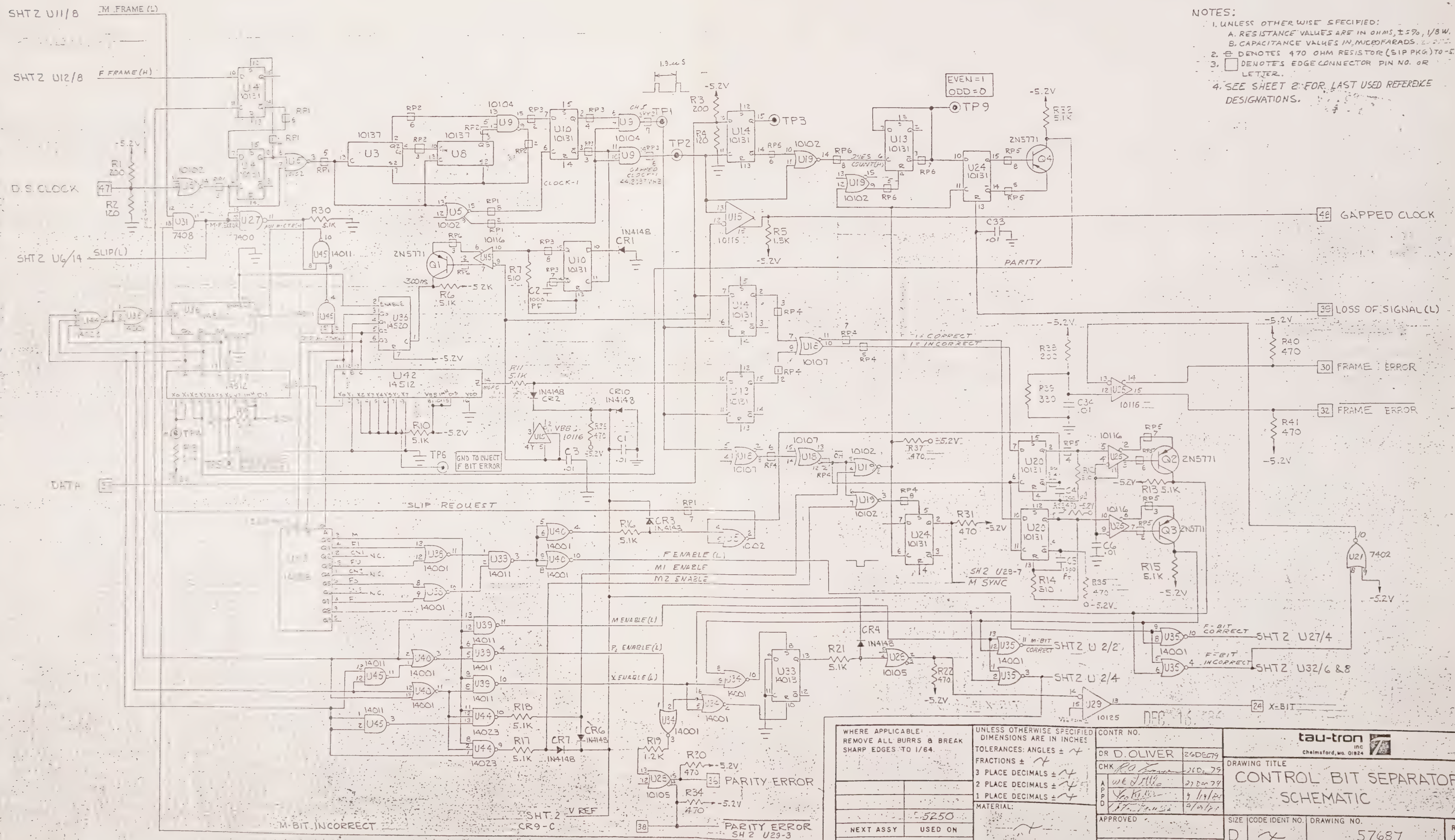


1. PLACE A PERMANANT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

WHERE APP. LABEL REMOVE ALL 8 PINS & BORE SHARP EDGES TO .004	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES = $\pm$ FRACTIONS = $\pm$ 3 PLATE DEC. MALS = $\pm$ 2 PLATE DEC. MALS = $\pm$ 1 PLATE DEC. MALS = $\pm$ MATERIAL:	DATE: DEC. 10, 1964 DR. G.A. CH. B.C. I.D. K.B. APPROVED BY: DIRECTION OF	tau-tron ORIGINAL FIGURE ASSEMBLY P.R. SYNC P.C. BD. SIZE: 1000 IDENT NO: DRAWING NO: D 954 57016 SCALE: 2/1 55200D RX 1 SHEET 1 OF 1	REV D
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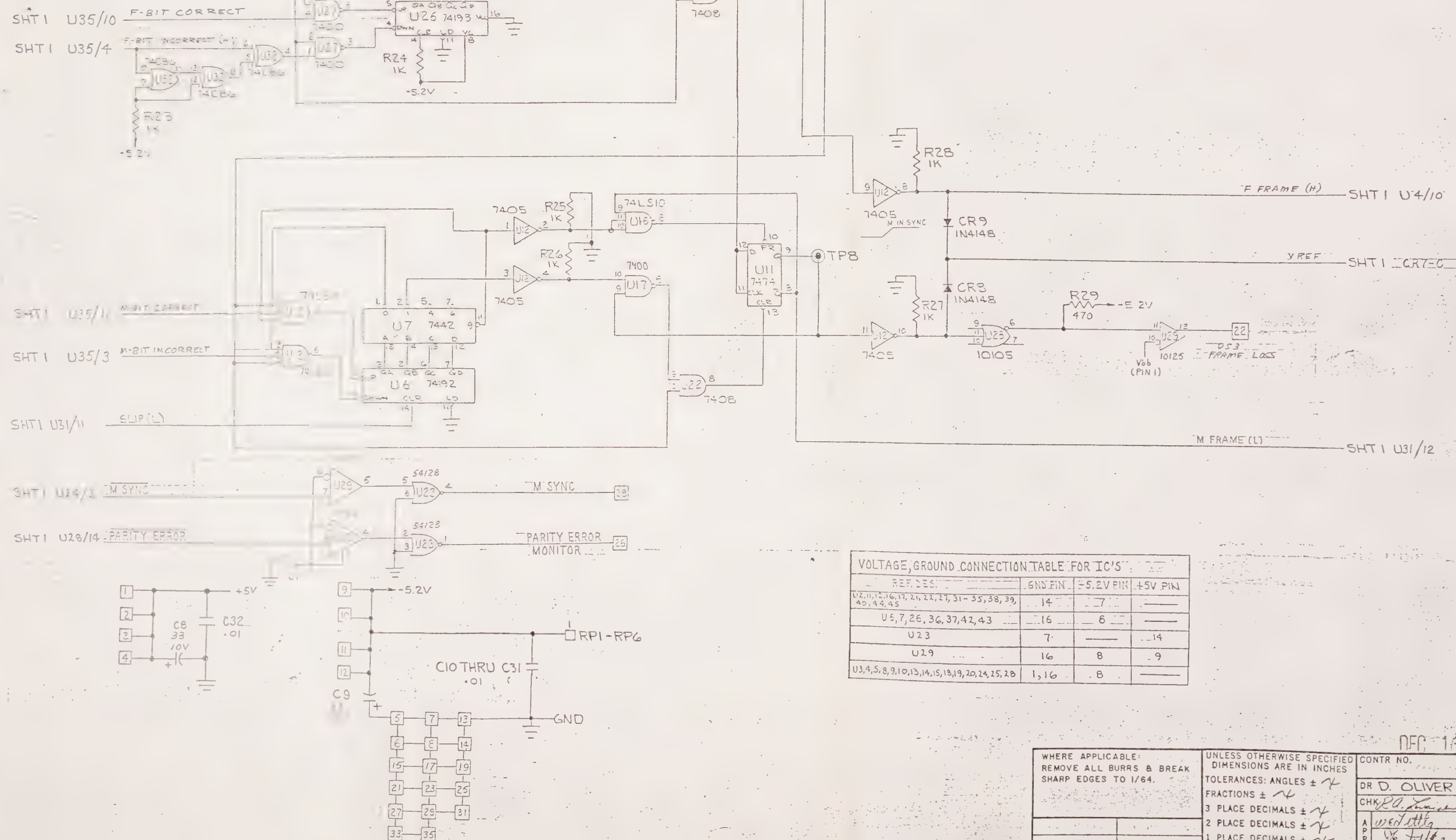
REVISIONS			
ZONE	LTR	DESCRIPTION	DATE
A	REL PER ECO #01968	9-14-84	9-17-84
B	UPDATED PER DOC ACCURACY PROG PER ECO #02178 7-9-85	7-9-85	8-11-85



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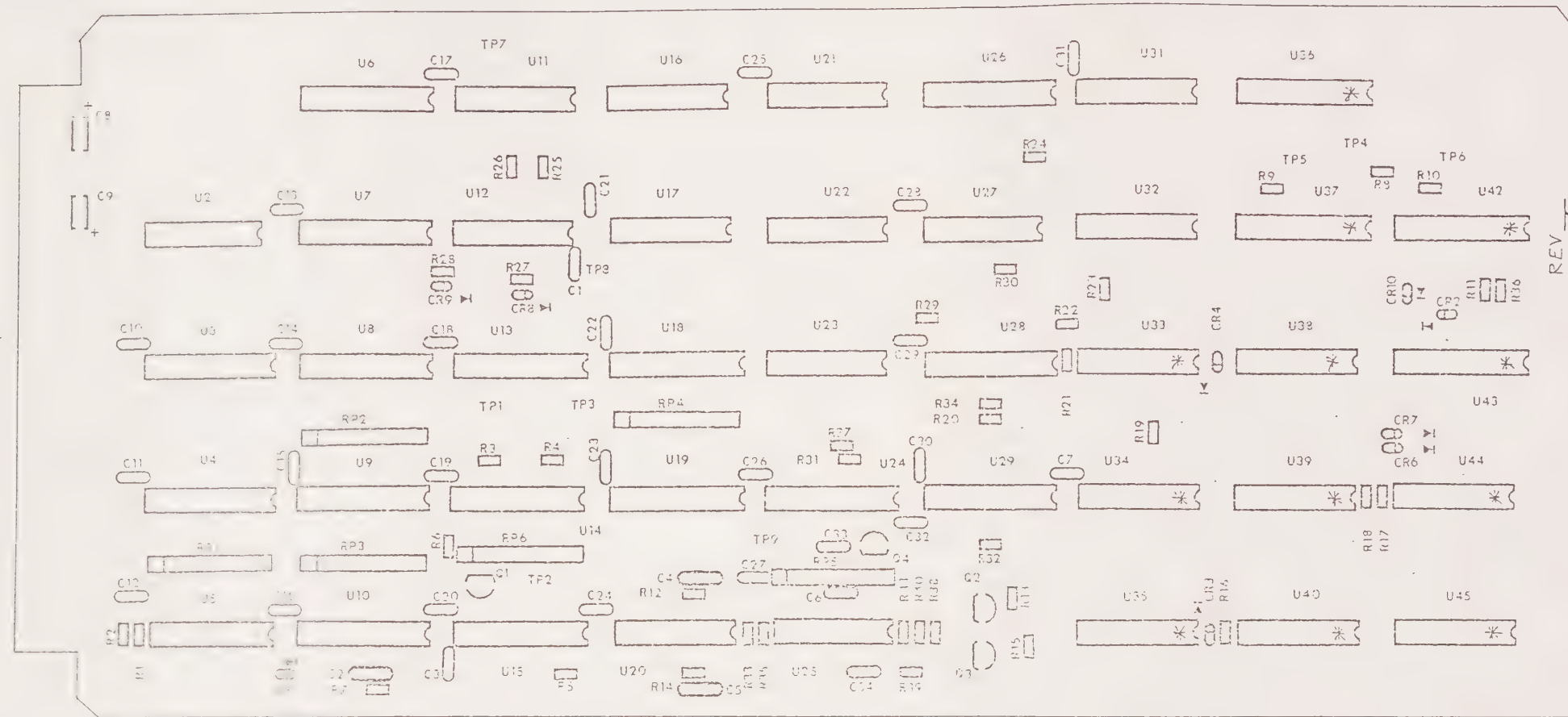
REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
		SEE SHEET 1 FOR REVISIONS		



VOLTAGE, GROUND CONNECTION TABLE FOR IC'S			
REF. DES.	GND PIN	-5.2V PIN	+5V PIN
U2, 11, 12, 16, 17, 21, 22, 27, 31 - 35, 38, 39, 40, 44, 45	14	7	
U6, 7, 26, 36, 37, 42, 43	16	8	
U23	7		14
U29	16	8	9
U3, 4, 5, 8, 9, 10, 13, 14, 15, 18, 19, 20, 24, 25, 28	1, 16	8	

LAST REF DES USED	UNUSED
U45	U130,41
C34	
R41	
Q4	
CR10	CR5
TP9	
RP6	

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CONTR NO.		tau-tron INC Chelmsford, MA 01824		DRAWING TITLE	
		TOLERANCES: ANGLES $\pm$ γ		DR D. OLIVER		26 DEC 79		CONTROL BIT SEPARATOR SCHEMATIC	
		FRACTIONS $\pm$ γ		CHK <i>P.A. Oliver</i>		16 DEC 79			
		3 PLACE DECIMALS $\pm$ γ		APP <i>W. J. Little</i>		37 DEC 79			
		2 PLACE DECIMALS $\pm$ γ		D <i>W. J. Little</i>		2/19/81			
		1 PLACE DECIMALS $\pm$ γ		MATERIAL: <i>St. 304</i>		5/19/81			
5250		MATERIAL: γ		APPROVED <i>/</i>		SIZE CODE IDENT NO.		DRAWING NO.	
NEXT ASSY		USED ON		BY DIRECTION OF <i>SH. M. Little</i>		D γ		576873	
APPLICATION						SCALE γ		5250	
								SHEET 2 OF 2	



SEE NOTE 2

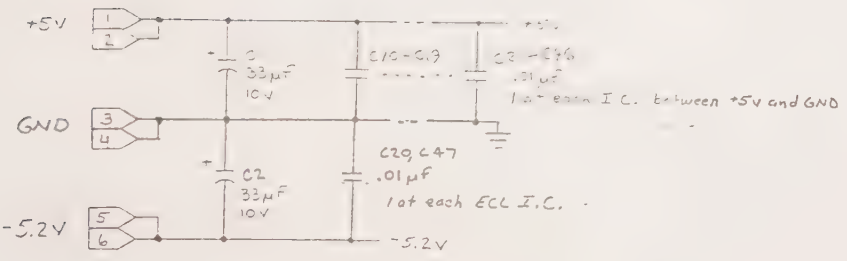
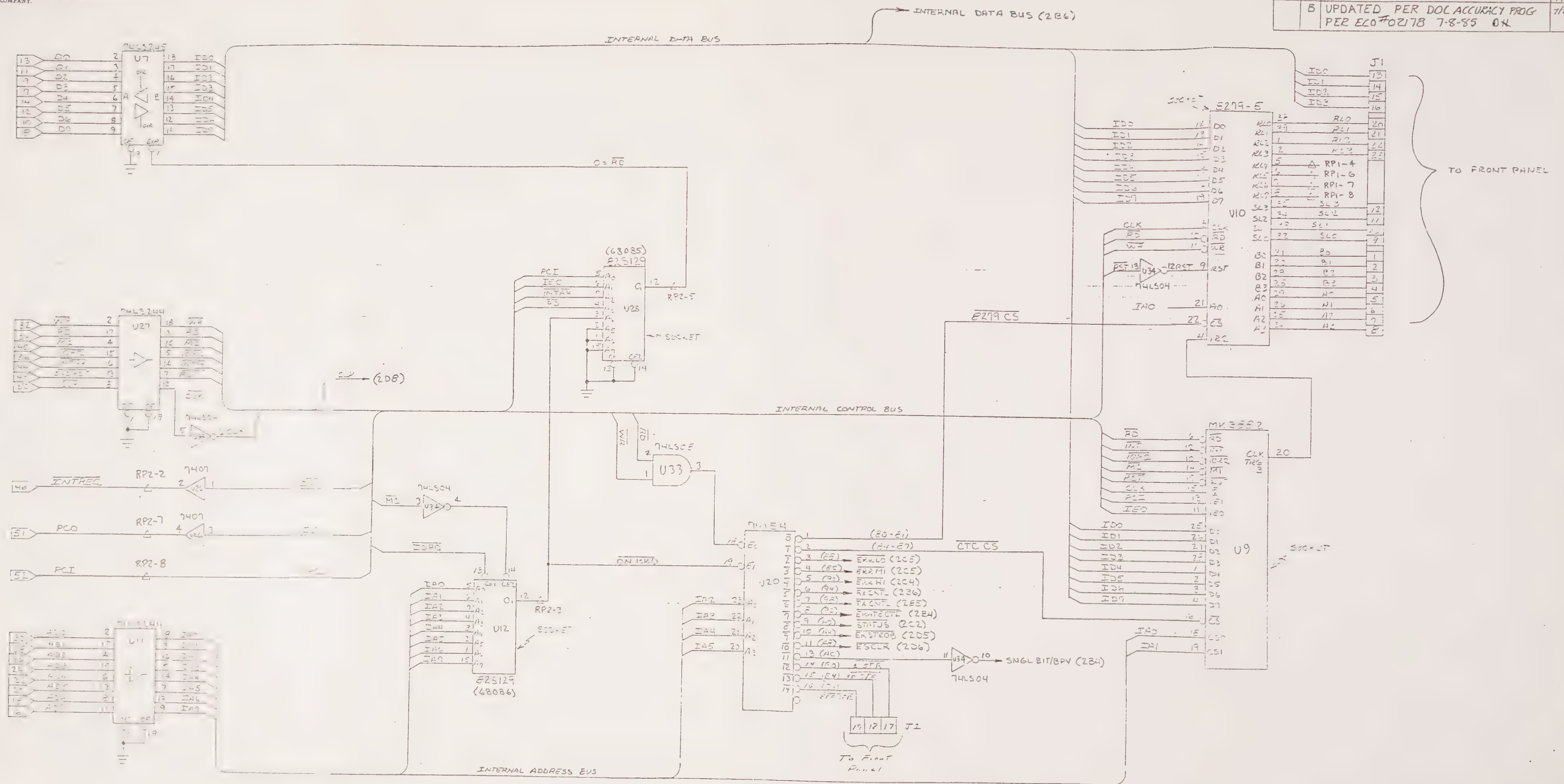
NOTES:

1. * DENOTES A STATIC SENSITIVE DEVICE.
2. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/32		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES = $\pm$ 1/2 FRACTIONS = $\pm$ 1/4 3 PLACE DECIMALS = $\pm$ 1/4 2 PLACE DECIMALS = $\pm$ 1/4 1 PLACE DECIMALS = $\pm$ 1/4		DATE: 16-1-84		DRAWING TITLE ASSEMBLY CONTROL BIT SEPARATOR	
57690 5250		MATERIAL: $\sim$		APPROVED: 9/1/84		REV B	
NEXT ASSY USED ON		APPLICATOR		BY: SHM 9/1/84		REV B	
				DATE: 2/1		57689 5250	

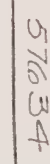
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REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROV
1			12-3-82	RL
2		ADD 5Y TO U16-14:3	8/25/84	RL
A		REL. TO PROD. ECO#01968	9/6/84	RL
B		UPDATED PER DOL ACCURACY PROG PER ECO#02178 7-8-85 OK	7/24/85	EX



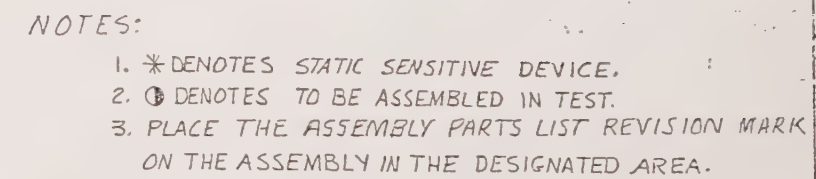
- NOTES:
- UNLESS OTHERWISE SPECIFIED:
 - RESISTANCE VALUES ARE IN OHMS $1/8W \pm 5\%$.
 - CAPACITANCE VALUES ARE IN MICROFARADS.
 - = Card edge connector.
 - = 4.7K 1/8W 131P to +5V.

REFERENCE DESIGNATIONS		WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± 4 FRACTIONS ± 4 3 PLACE DECIMALS ± 4 2 PLACE DECIMALS ± 4 1 PLACE DECIMALS ± 4 MATERIAL:	CONTR NO. 10-24	tau-tron inc Chelmsford, MA 01824
LAST USED	UNUSED				
C49					
Q3					
R15					
R32					
U33					
J3					
		10-57634	S5250		
		NEXT ASSY	USED ON		
		APPLICATION			
				APPROVED	
				BY DIRECTION OF	
				5/11/84	
				SCALE	
				S5250	
				DRAWING NO.	
				57634	
				REV	
				B	
				SHEET 1 OF 2	



Note: Use Ground Plane

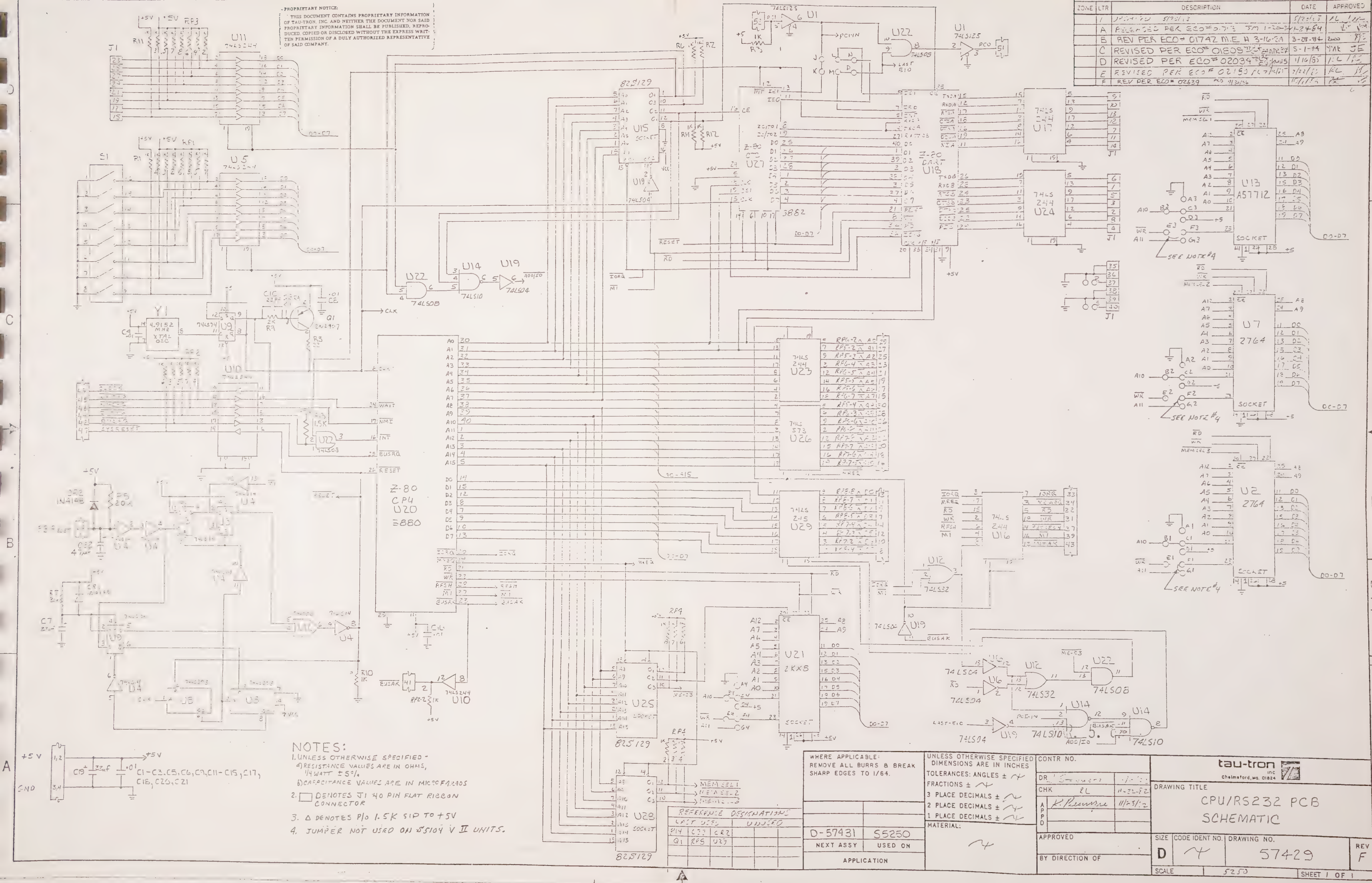
REV
B



WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES $\pm$ γ FRACTIONS $= \gamma$ 3 PLACE DECIMALS $\pm$ γ 2 PLACE DECIMALS $= \gamma$ 1 PLACE DECIMALS $\pm$ γ MATERIAL: γ		CONTR NO DEC. 16, 1984 DR B.M.B. 9-7-83 CHK H.L.L. 9-8-83 APP AS 7-8-83 CD JE 9-19-84 ac 9-19-84 APPROVED BY DIRECTION OF SHH 9-9-84		tau-tron QUALITY GENERAL SIGNAL DRAWING TITLE ASSEMBLY PERIPHERAL CNTL/CTR 55250 SIZE (CODE) IDENT NO. DRAWING NO. D 57636 SIZE 2/1 55250		REV C SHEET 1 OF 1	
552050 NEXT ASSY USED ON APPLICATION									

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ZONE	LTR	DESCRIPTION	DATE	APPROVED
1	1	INITIAL	5/21/82	RL
A	1	REVISED PER ECO#01713	5/21/82	RL
B	1	REV PER ECO#01742 M.E. H 3-16-84	3-28-84	RL
C	1	REVISED PER ECO#01805	5-1-84	RL
D	1	REVISED PER ECO#02034	1/16/85	RL
E	1	REVISED PER ECO#02150	7/22/85	RL
F	1	REV PER ECO#02639	11/11/85	RL



- NOTES:
1. UNLESS OTHERWISE SPECIFIED -
A) RESISTANCE VALUES ARE IN OHMS,
1/4WATT ±5%
B) CAPACITANCE VALUES ARE IN MICROFARADS
 2. □ DENOTES J1 40 PIN FLAT RIBBON CONNECTOR
 3. Δ DENOTES P10 1.5K SMD TO +5V
 4. JUMPER NOT USED ON 85104 V II UNITS.

REFERENCE DESIGNATIONS

LAST USED UNUSUED

P14 C77 C82
Q1 RPS U29

WHERE APPLICABLE:
REMOVE ALL BURRS & BREAK
SHARP EDGES TO 1/64.

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES: ANGLES ± 1/2°
FRACTIONS ± 1/16
3 PLACE DECIMALS ± 0.001
2 PLACE DECIMALS ± 0.01
1 PLACE DECIMALS ± 0.1
MATERIAL:

CONTR. NO.

DR. S. H. H. 11-21-82

CHK. R. L. 11-21-82

A. P. D. 11-21-82

APPROVED

BY DIRECTION OF

tau-tron
INC
Chelmsford, MA 01824

DRAWING TITLE
CPU/RS232 PCB
SCHEMATIC

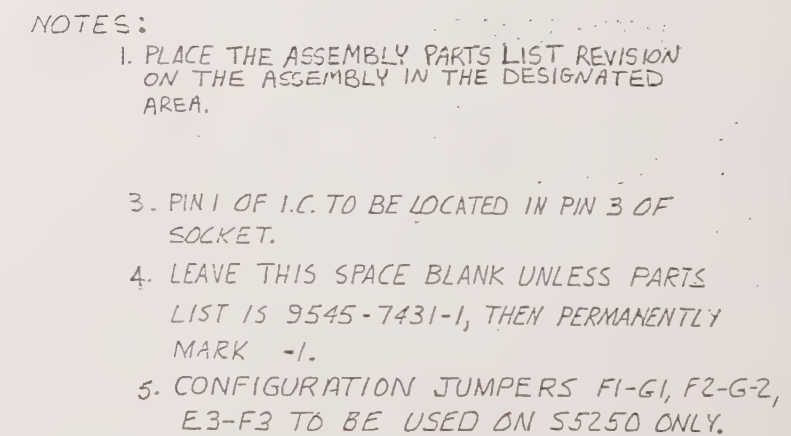
SIZE CODE IDENT NO. DRAWING NO.

D 57429

SCALE 5250

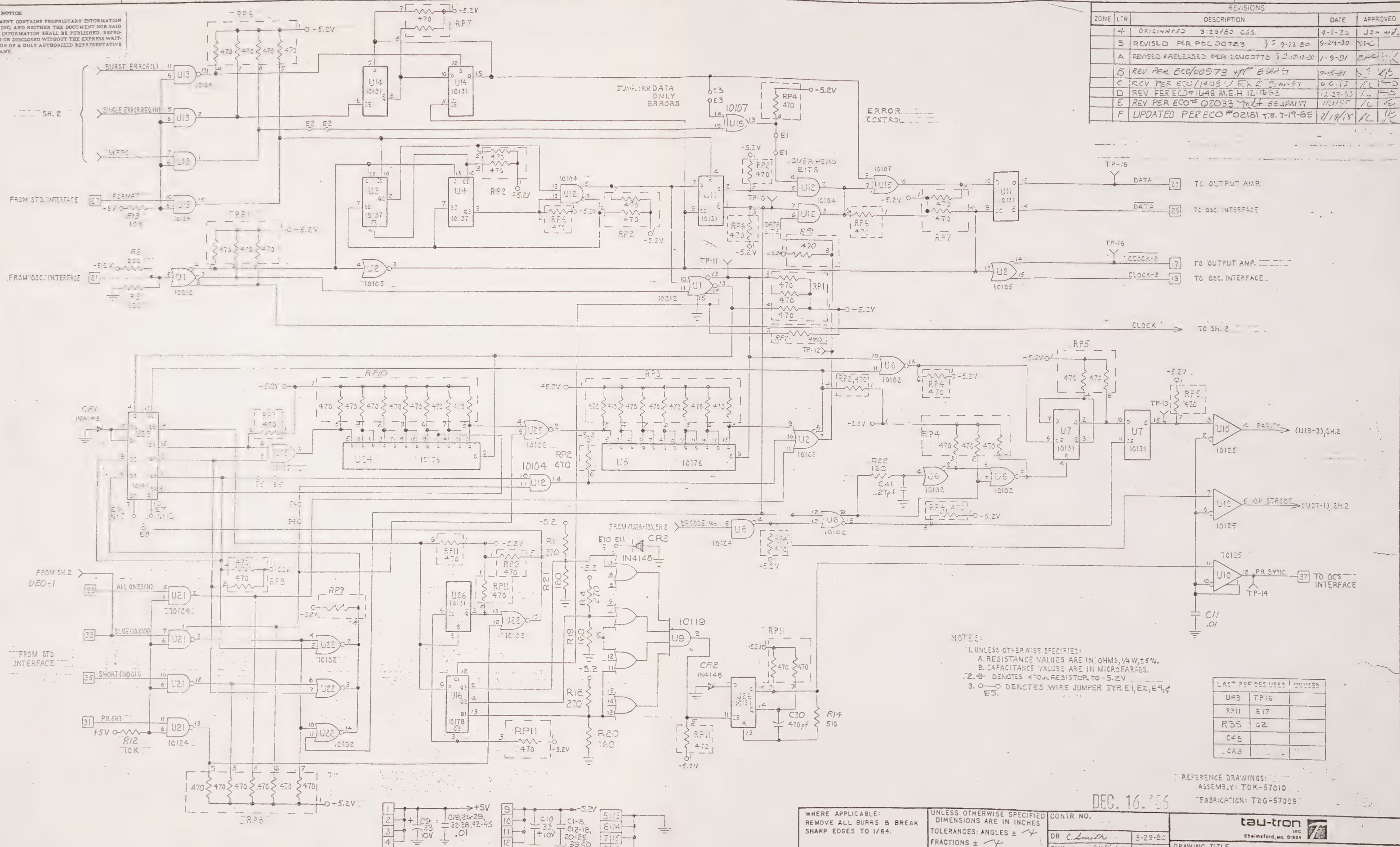
SHEET 1 OF 1

REV
F



WHERE APPLICABLE REMOVE ALL BURRS & BRUISES SHARP EDGES TO 1/64"		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		DATE DEC. 16, 1982		tau-tron ELECTRONIC GENERAL SIGNAL	
		TOLERANCES ANGLES ± 1/4		DR. S.M.B.		DRAWING TITLE	
		FRACTIONS ± 1/2		CHK. H.D.L.		ASSEMBLY	
		3 PLACE DECIMALS = 1/4		11-29-82		CPU/RS232 PCB	
		2 PLACE DECIMALS = 1/2		K.B.		SS104	
		1 PLACE DECIMALS ± 1/2		11-29-82			
D-57626 SS250		MATERIAL:		APPROVED		ONE IDENT NO. DRAWING NO.	
E-57469 SS104 VII		SEE P/L				D 57431	
NEXT ASSY USED ON		9545-7431		BY: DIRECTOR OF		57431	
APPLICATION						SCALE 1:1	
						SHEET 1 OF 1	

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REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
4		ORIGINATED 3/23/80 C.S.	4-7-80	JEM wpl
5		REVISED PER PCC00723 9-22-80	9-24-80	YFC
A		REVISED & RELEASED PER EC000770 5-10-80	1-9-81	YFC
B		REV. PER EC000973 4/10/81	9-15-81	YFC
C		REV. PER EC011408 1/12/82	6-6-82	YFC
D		REV. PER EC011648 M.E.H. 12-16-82	12-27-82	YFC
E		REV. PER EC010203 7/1/83	11-1-83	YFC
F		UPDATED PER EC0102181 T.B. 7-19-85	7/19/85	YFC

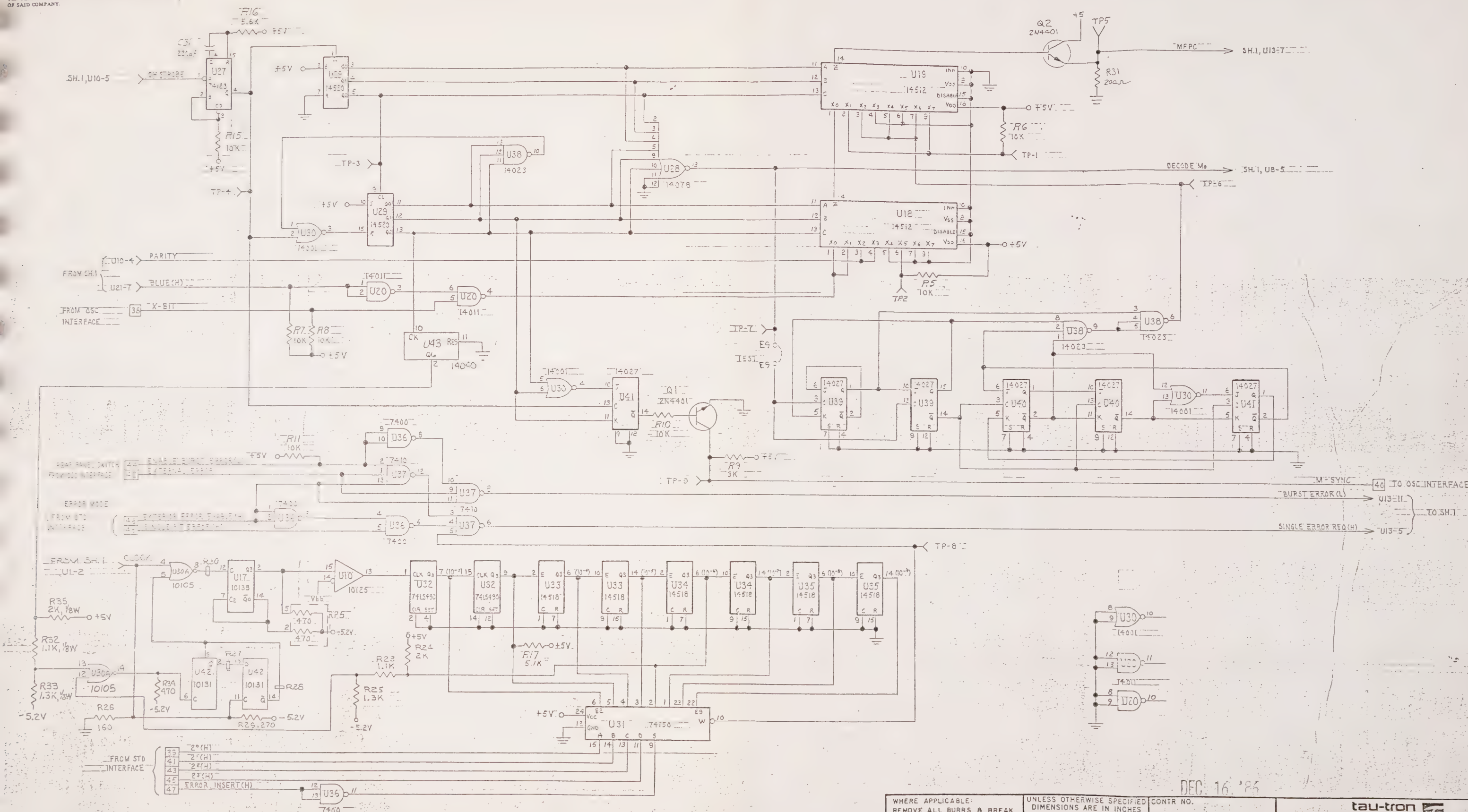
- NOTES:
- UNLESS OTHERWISE SPECIFIED:
 - RESISTANCE VALUES ARE IN OHMS, 1/4W, ±5%.
 - CAPACITANCE VALUES ARE IN MICROFARADS.
 - Ø DENOTES 470Ω RESISTOR TO -5.2V
 - DENOTES WIRE JUMPER TYP. E1, E2, E4, E5.

LAST REF DES USED		UNUSED
U43	TP16	
RP11	E17	
R35	Q2	
C45		
CR3		

REFERENCE DRAWINGS:
ASSEMBLY: TDK-57010
FABRICATION: TDK-57009

DEC. 16, '86

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES ± 4 FRACTIONS ± 4 3 PLACE DECIMALS ± 4 2 PLACE DECIMALS ± 4 1 PLACE DECIMALS ± 4 MATERIAL:	CONTR. NO. DR C. Smith 3-29-80 CHK R.C. Smith 4-4-80 APPROVED BY DIRECTION OF	tau-tron INC. Chelmsford, MA 01824
TDK-57010 S52000 TX NEXT ASSY USED ON	DRAWING TITLE SCHEMATIC TRANSMIT GENERATOR PCB	SIZE CODE IDENT NO. DRAWING NO. D 57008	REV F
APPLICATION	SCALE S52000 TX	SHEET 1 OF 1	

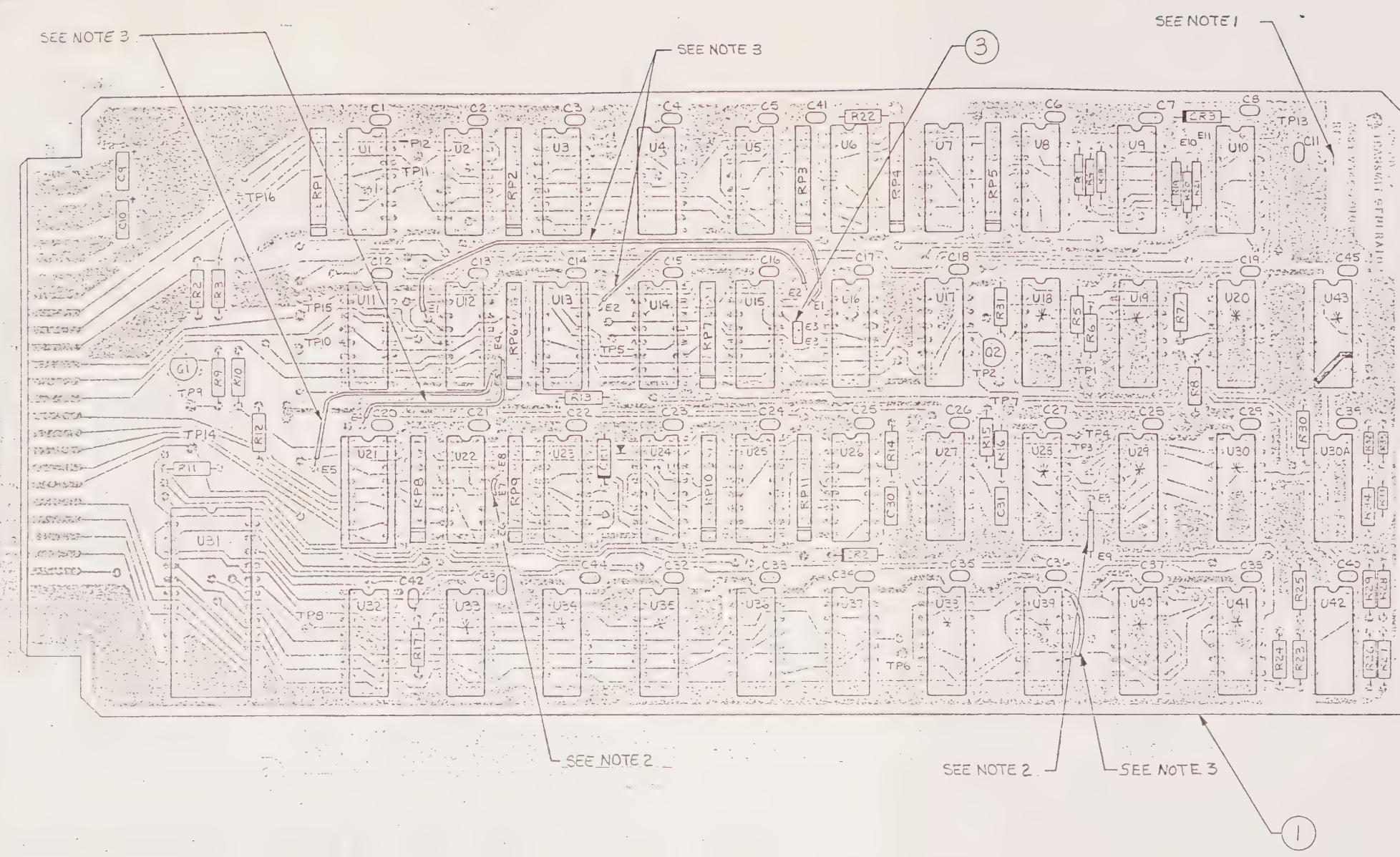


DEC 16 '82

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CONTR NO.		tau-tron inc chilmsford, wa. 0124	
		TOLERANCES: ANGLES ± 1/4		DR C. Smith 3-29-80		DRAWING TITLE SCHEMATIC TRANSMIT GENERATOR PCB	
		FRACTIONS ± 1/4		CHK			
		3 PLACE DECIMALS ± 1/4		APP W. Little 4-9-80			
		2 PLACE DECIMALS ± 1/4		P			
		1 PLACE DECIMALS ± 1/4					
TDK-57010... 55200D TX		MATERIAL:		APPROVED		SIZE CODE IDENT NO. DRAWING NO.	
NEXT ASSY USED ON		4				D 7 57008	
APPLICATION				BY DIRECTION OF		SCALE 1" = 1" SHEET 2 OF 2	

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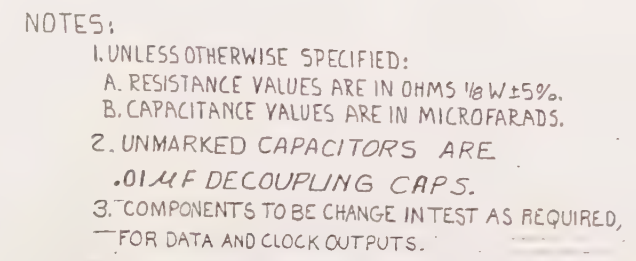
H	REV PER ECO# 02033	TMM 1-24-85	1-25-85	KL	JS
J	UPDATED PER DOC ACCURACY PROG PER ECO# 02178	6-28-85	7-29-85	KL	MB
K	UPDATED PER ECO# 02181 TB	8-8-85	8-14-85	KL	KL
L	REV PER ECO# 02656	MCD 10/27/86	10/27/86	PK	JE



1. PLACE A PERMANENT REVISION LEVEL MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.
2. JUMPERS TO BE #24 AWG BUS WIRE WITH SLEEVING.
3. JUMPERS TO BE #30 AWG INSULATED WIRE.
4. *- DENOTES STATIC SENSITIVE DEVICE.

DEC. 10. 85		ASSEMBLY	
J.T.	12-31-80	TRANSMIT GENERATOR PCB	
B.COOPER	1-9-81		
W.ROLLINS	1-20-81		
S5200D TX		954 57010	
SEE P. L. 9545-7010		S5200D TX	

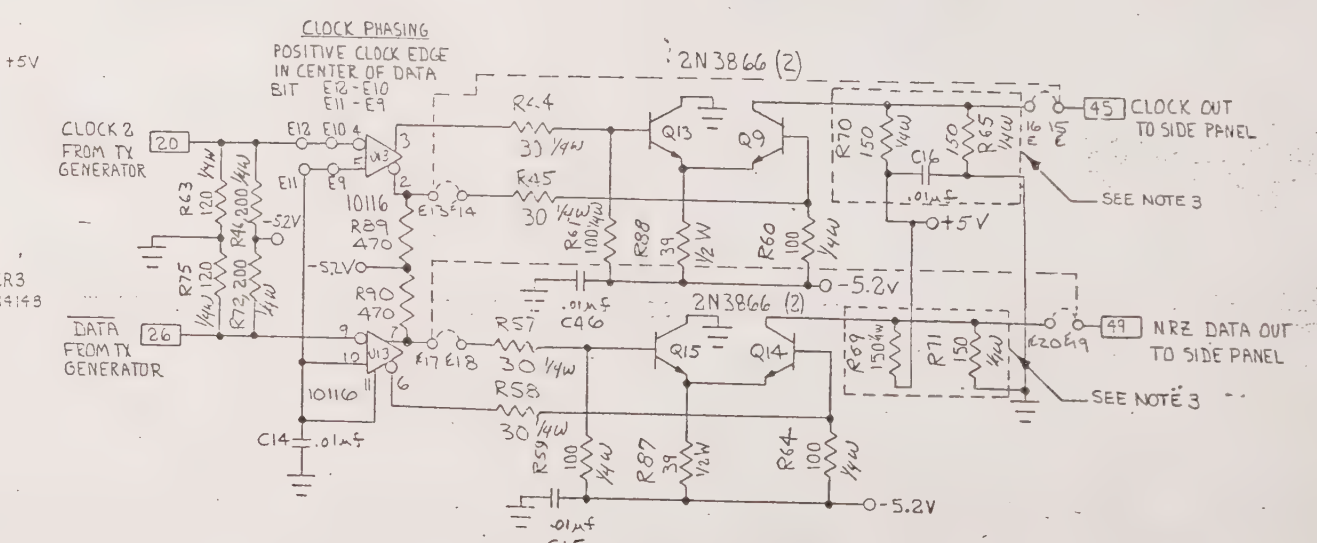
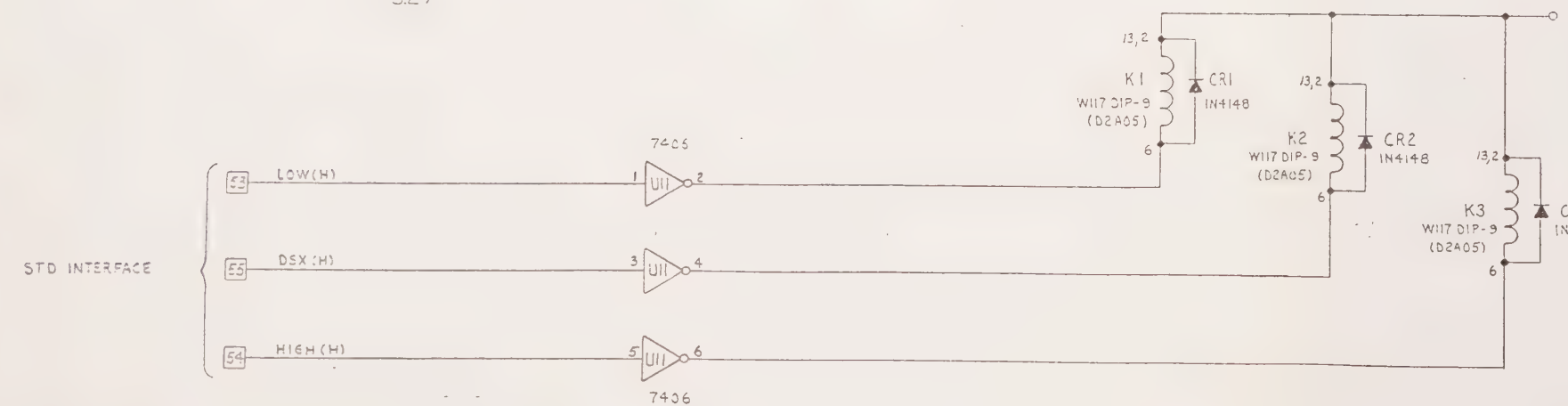
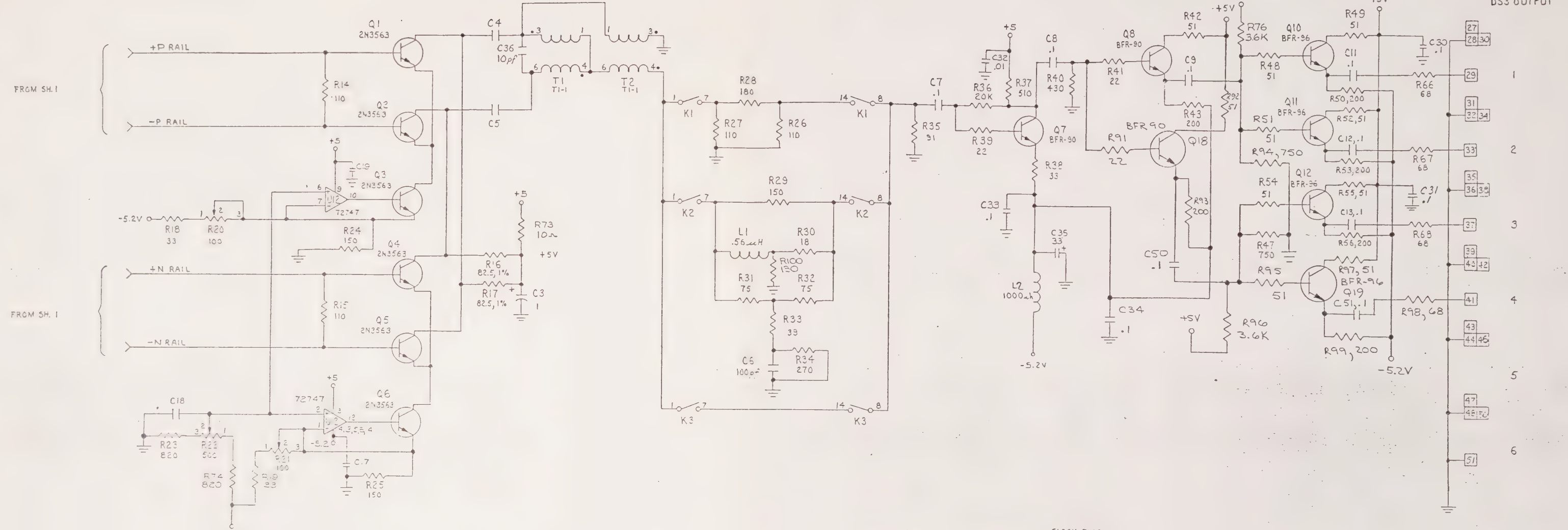
57010



REFERENCE		DESIGNATIONS		WHEN APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CONTR NO.		tau-tron INC Chelmsford, MA 01824	
LAST USED		UNUSED				TOLERANCES: ANGLES ±		DR <i>Bryant M. Bawlin</i> 7-26-83		DRAWING TITLE	
CS1	R3	C47				FRACTIONS ±		CHK <i>HP</i>	7-26-83	SCHEMATIC OUTPUT AMPLIFIER / OSC	
CR3	T2	C47				3 PLACE DECIMALS ±		A <i>Residential</i>	8-1-83		
L3	U14	C49				2 PLACE DECIMALS ±		P <i>K. Stille</i>	9-19-83		
OP	Y1					1 PLACE DECIMALS ±		D <i>W. H. H. H.</i>	2-13-84		
R100					55250	MATERIAL:		APPROVED /		SIZE	CODE IDENT NO.
RP4				NEXT ASSY	USED ON			BY DIRECTION OF <i>SA MacLean</i>		D	57643
APPLICATION										SCALE	SHEET 1 OF 2

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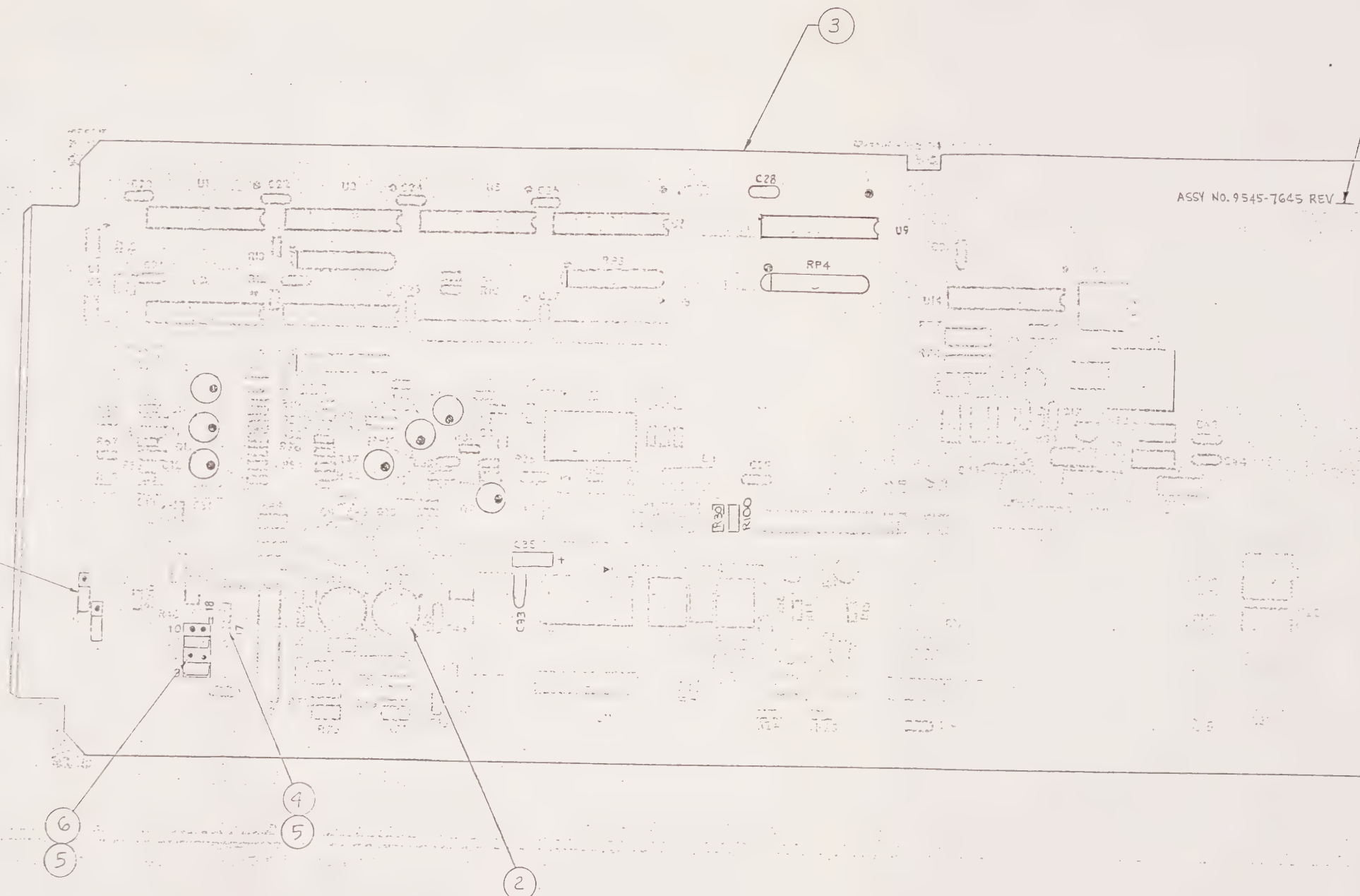
REVISIONS		DATE	APPROVED
ZONE	LTR	DESCRIPTION	
SEE SHEET 1 FOR REVISIONS			



WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		CONTR NO. 55250		tau-tron INC Chelmsford, MA 01924	
TOLERANCES: ANGLES ±		FRACTIONS ±		CHK	DATE	DRAWING TITLE	
3 PLACE DECIMALS ±		2 PLACE DECIMALS ±		APPROVED	DATE	SCHEMATIC	
1 PLACE DECIMALS ±		MATERIAL:		BY DIRECTION OF	DATE	OUTPUT AMPLIFIER/OSC	
55250		NEXT ASSY USED ON		SA [Signature] 9/12/84		SIZE	DRAWING NO.
APPLICATION						D	57643
						SCALE	SHEET 2 OF 2

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2	UPDATED 5-31-84 <i>Re. Ruckman</i>	5-31-84	<i>XL</i>
3	UPDATED <i>Re. Ruckman</i> 8-13-84	8-14-84	<i>XL</i>
4	UPDATED C28 & C25 BUDD 5 SEP 84		
A	REL. TO PROD. ECO #01968 BUDD 5 SEP 84	9/6/84	<i>XL</i> <i>PK</i>
B	REV PER ECO #02116 4/22/85	5/1/85	<i>PK</i> <i>PK</i>
C	UPDATED PER DOC ACCURACY PROG PER ECO #02178 7-1-85 <i>DN</i>	7/23/85	<i>PK</i> <i>PK</i>
D	REV PER ECO #02409 8/21/86	8-16-86	<i>XL</i> <i>XL</i>



NOTES:
1. PLACE THE ASSEMBLY PARTS LIST REVISION
MARK ON THE ASSEMBLY IN THE DESIGNATED AREA.

WHERE USED: 9545-7645		MINOR CHANGES TO PREVIOUS EDITIONS		DATE	
REVISIONS TO 3-8-84		TOLERANCES: 1/16" 1/32" 1/64"		BUDD 2-8-84	
DRAWN BY: 81		PLACE DIMENSIONS		A.J. SENECA 9-1-83	
		PLACE DIMENSIONS		4-19-84	
		PLACE DIMENSIONS		9-19-84	
S5250		SEE P/L		9545-7645	
APPLICATION				57645	
				S5250	

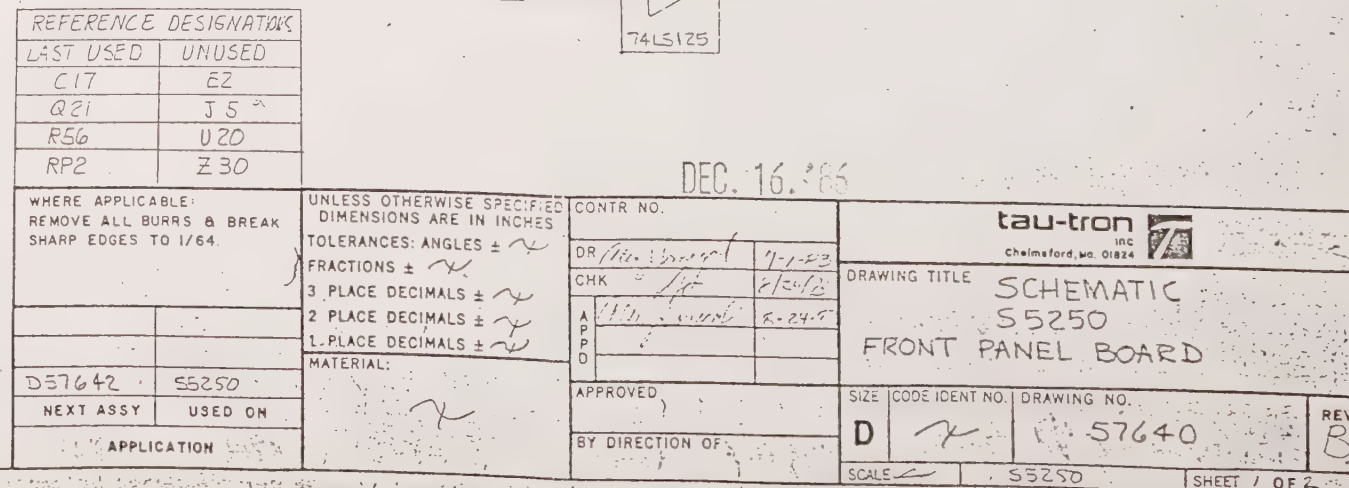
DEC. 16 1984

ASSEMBLY
OUTPUT AMP PCB
S5250

57645

S5250

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
	1	UPDATED ORIGINAL	BUDG 18 AUG 85	AL
	2	REJISED PER PROTOTYPE	BUDG 29 DEC 85	AL
	3	REF #MS 229 (LIVE)	PJL 73 MAR 89	AL
	4	REV P15 ECO #CM15	BUDG 27 JUN 84	AL
	B	PER DOL ACCURACY #ROG	7-6-84	PFE
		PER ECO #02178	7/6/85	FX
		7-9-85		JF



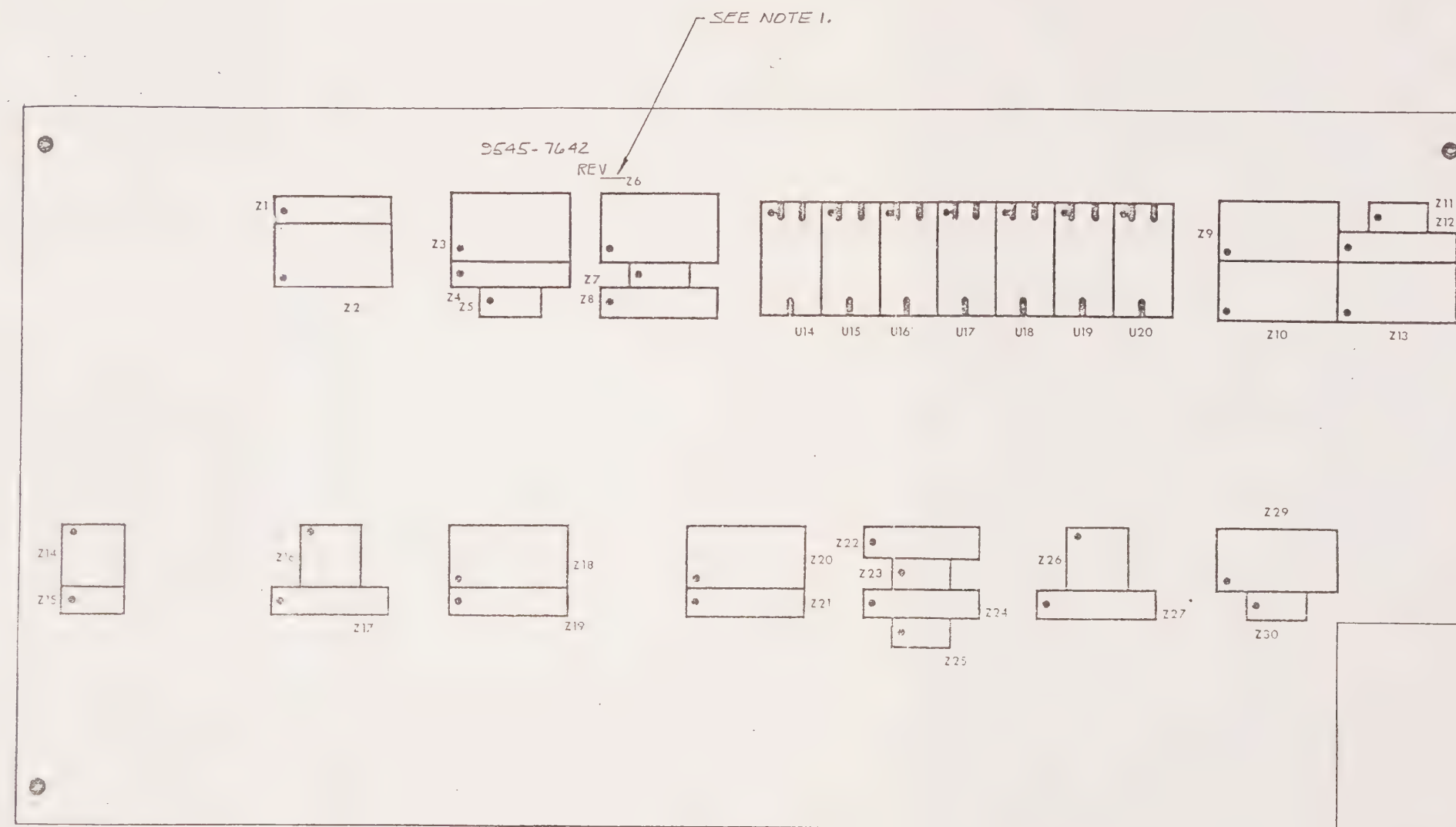
1. UNLESS OTHERWISE SPECIFIED:
A. RESISTANCE VALUES ARE IN OHMS $1/4W$, 5%
B. CAPACITANCE VALUES ARE IN MICROFARADS.

2.  DENOTES 150 OHM $1/4W$, 5% RESISTOR TO +5V.

3.  DENOTES 4.7K OHM $1/4W$, 5% RESISTOR TO +5V.

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LINE	REV	DESCRIPTION	DATE	BY
A		REL PER ECO# 1915 TMM 7-2-82	7-6-84	JE / 1915
B		UPDATED PER DOC ACCURACY PRG PER ECO# 02178 6-28-85 @ 15	7/28/85	JE



COMPONENT SIDE SCREEN
57641 B

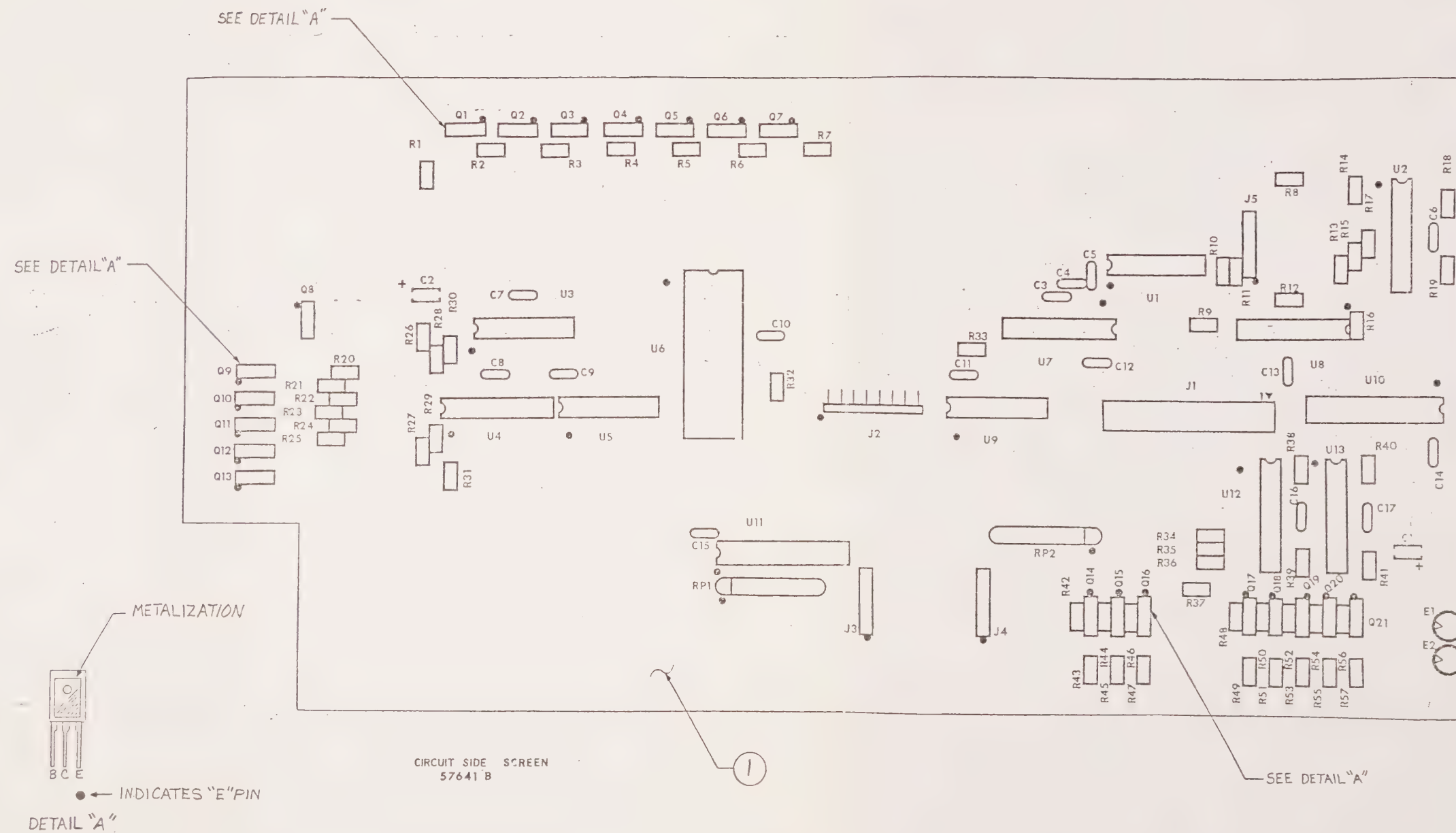
NOTES:
1. PLACE A PERMANENT REVISION LEVEL MARK
ON THE ASSEMBLY IN THE DESIGNATED AREA

DEC. 16. 82

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES = $\sim$ FRACTIONS = $\sim$ 3 PLACE DECIMALS = $\sim$ 2 PLACE DECIMALS = $\sim$ 1 PLACE DECIMALS = $\sim$ MATERIAL		CONTR NO DR BMB 8-26-83 CHK RJL 8-31-83 AS. 8-31-83		tau-tron Chelmsford, MA 01824	
S5250		APPROVED		SIZE (CODE CENTER) DRAWING NO		REV	
NEXT ASSY USED ON		BY DIRECTION OF		D 954 57642		B	
APPLICATION				SCALE 2/1 S5250		SHEET 1 OF 2	

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SEE SHEET 1 FOR REVISION

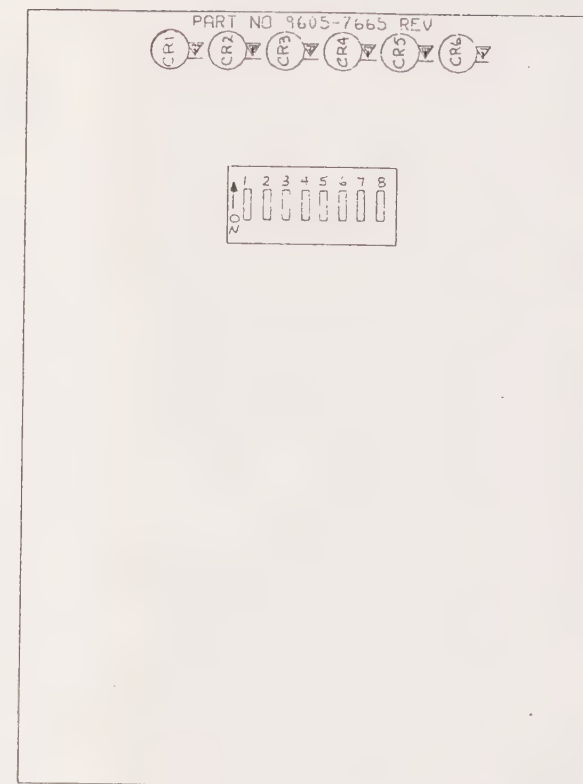
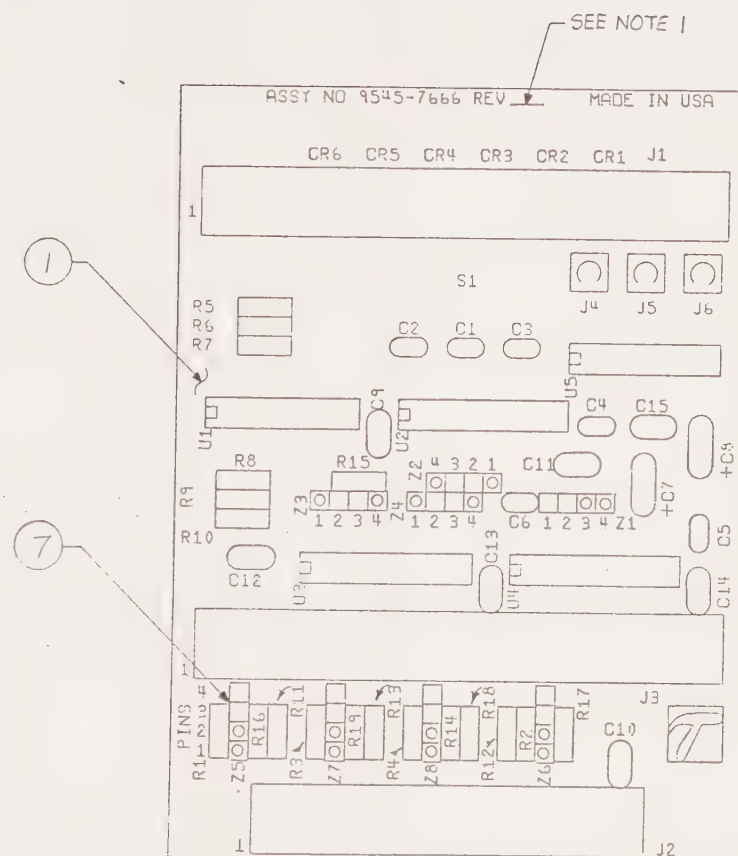


DEC. 16. '86

WHERE APPLICABLE REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ANGLES ± FRACTIONS ± 3 PLACE DECIMALS ± 2 PLACE DECIMALS ± 1 PLACE DECIMALS ± MATERIAL.		CONTRACT NO. DR J. MILLEY CHK APPROVED BY DIRECTION OF		tau-tron INC. Chelmsford, MA 01824	
S5250				1-11-84		DRAWING TITLE ASSEMBLY FRONT PANEL PCB.	
NEXT ASSY USED ON APPLICATION						SIZE CODE DENT NO DRAWING NO D 954 57642	
						SCALE 2/11 S5250 SHEET 2 OF 2	

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REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROV
2		UPDATED TMM	7-25-84	8/4/84
3		UPDATED PER 8-23-84	8-24-84	9/1/84
A		RELEASED PER ECO #01990 TMM	10-4-84	10-10-84
B		UPDATED PER DOC ACCURACY PROG PER ECO #02178 7-1-85	7/27/85	PK



NOTES:

1. PLACE A PERMANENT REVISION LEVEL MARK
ON THE ASSEMBLY IN THE DESIGNATED AREA.

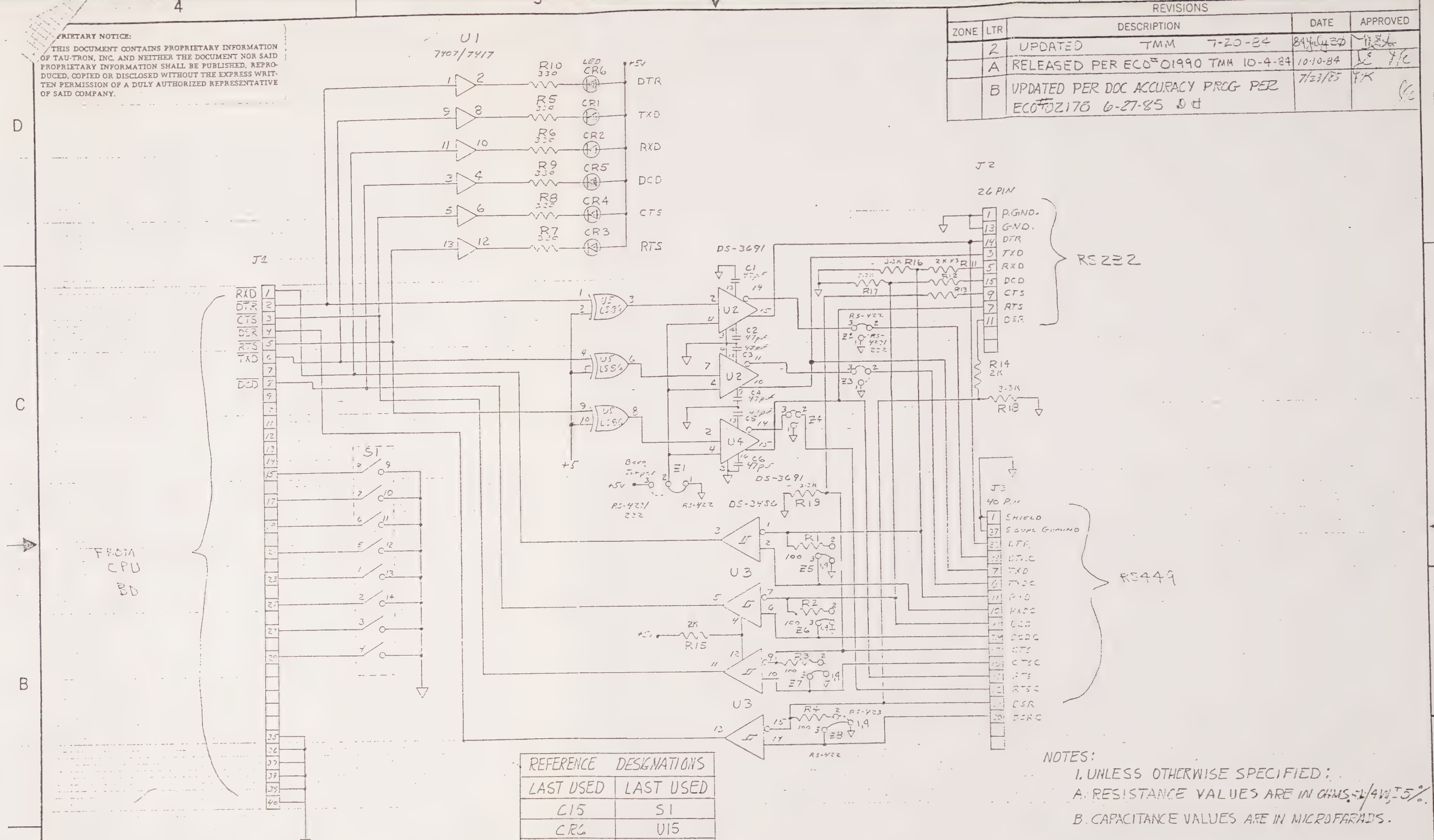
PATCH TABLE

PATCH	RS232	RS449/422	RS449/423
Z1	1-2	1-2	2-3
Z2	2-3	2-3	1-2
Z3	2-3	2-3	1-2
Z4	2-3	2-3	1-2
Z5	3-4	2-3	3-4
Z6	3-4	2-3	3-4
Z7	3-4	2-3	3-4
Z8	3-4	2-3	3-4

WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES $\pm .4$ FRACTIONS $\pm .4$ 3 PLACE DECIMALS $\pm .4$ 2 PLACE DECIMALS $\pm .4$ 1 PLACE DECIMALS $\pm .4$ MATERIAL:		CONTR NO. DEC-16		tau-tron INC Chelmsford, MA 01824	
57680/81		S5250		DRED MENDED 3-21-84		DRAWING TITLE ASSEMBLY RS-232 RS-449 INTERFACE BD.	
NEXT ASSY		USED ON		CHKD 10-13-84		SIZE CODE IDENT NO. DRAWING NO.	
APPLICATION		BY DIRECTION OF		APPROVED 10-10-84		D 954 57666	
				SCALE 2/1		S5250 SHEET 1 OF 1	

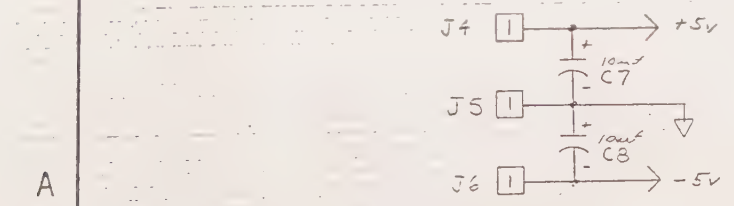
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REVISIONS					DATE	APPROVED
ZONE	LTR	DESCRIPTION	TMM			
	2	UPDATED	7-20-84	8410430		
	A	RELEASED PER ECO#01990 TMM 10-4-84	10-10-84			
	B	UPDATED PER DOC ACCURACY PROG PER ECO#02176 6-27-85 D H	7/23/85			



REFERENCE	DESIGNATIONS
LAST USED	LAST USED
C15	S1
CR6	U15
J3	Z8
R19	

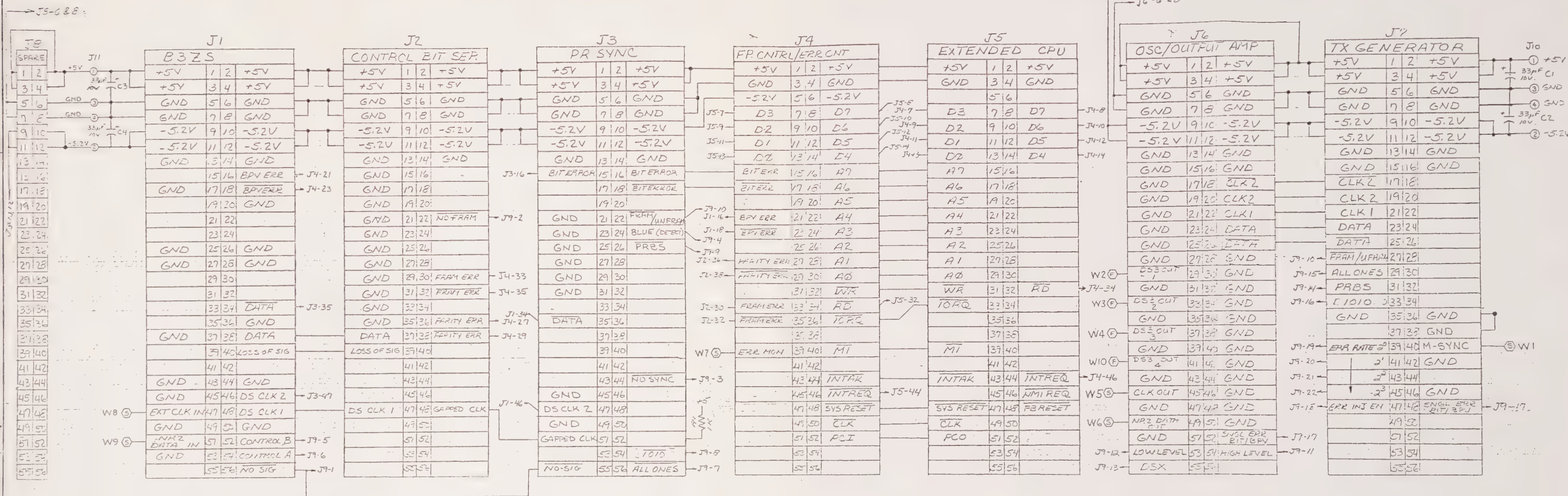
NOTES:
1. UNLESS OTHERWISE SPECIFIED:
A. RESISTANCE VALUES ARE IN OHMS. 1/4W. 5%
B. CAPACITANCE VALUES ARE IN MICROFARADS.



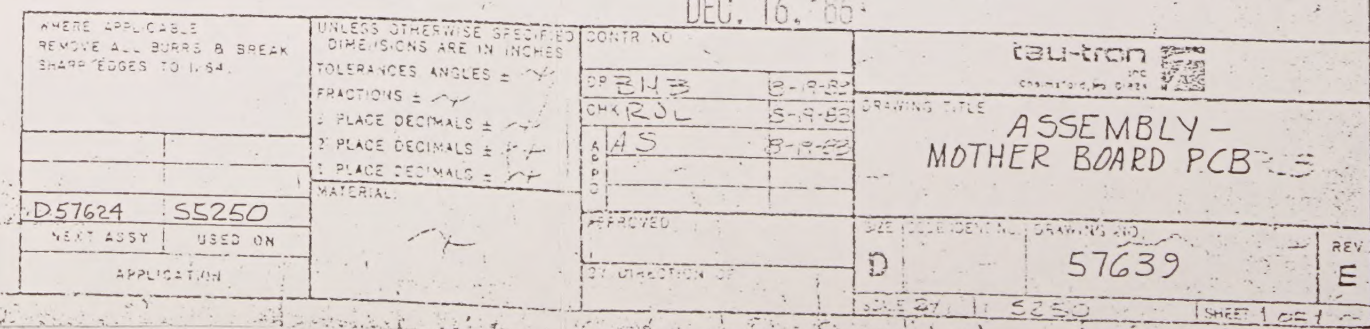
WHERE APPLICABLE: REMOVE ALL BURRS & BREAK SHARP EDGES TO 1/64.		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: ANGLES $\pm$ 1/4 FRACTIONS $\pm$ 1/4 3 PLACE DECIMALS $\pm$ 1/4 2 PLACE DECIMALS $\pm$ 1/4 1 PLACE DECIMALS $\pm$ 1/4 MATERIAL:		CONTR NO. DR <i>SA [signature]</i> 3/1/84 CHK <i>SA [signature]</i> 10-10-84 APP <i>SA [signature]</i> 10-10-84 BY DIRECTION OF		tau-tron inc Chelmsford, Ma. 01824	
D-57666 S5250 NEXT ASSY USED ON		APPLICATION		DRAWING TITLE RS-232/RS-449 INTERFACE BD.		REV B	
SIZE C		CODE IDENT NO. 4		DRAWING NO. 57664		SHEET 1 of 1	

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ZONE		LTR		DESCRIPTION	DATE	APPROVED
A	REL PER ECO#01915 M.E.H	7-6-81	2-22-81	7-6-81	2-22-81	2-22-81
B	REV PER ECO#01934 M.E.H	8-15-81	7-1-81	7-1-81	7-1-81	7-1-81
C	UPDATED PER DOC ACCURACY PROG PER ECO#02178 7-9-85 DN					



DATE	DESCRIPTION	DATE	DESCRIPTION
A	REV PER ECO # 01918 MCH 7/6/80	7/6/80	PK
B	REV PER ECO # 01954 B/ 7/13/80	7/13/80	PK
C	UPDATED PER DUCAL RAIL PRG PER ECO # 02173 6-23-85 C #	7/21/85	PK
D	REV PER ECO # 02328 MCD 7/2/86	7/1/86	PK
E	REV PER ECO # 02580 MCD 8/14/86	8/18/86	PK





82228